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low if the two inputs are (A) 00 (B) 01 (C) 10 (D) 11 Ans: D The NAND gate output will be low if the two inputs are 11

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Since there are more than one outputs and number of outputs is less than inputs, it is a Priority encoder  $V=1$  when input is valid and for priority encoder it checks first high bit encountered. Except all are having at least one bit high and 'x' represents the "don't care" as we have found a high bit already.

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